



# PPG INSTITUTE OF TECHNOLOGY

*Empowering total Technology*

Approved by

AICTE, New Delhi,

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Coimbatore -641 035.



## Department of Electronics and Communication Engineering

Academic Year: 2022- 2023 (Even Semester)

### Innovative Teaching Learning Practices – Participative Learning

Degree, Semester& Branch	: BE ECE
Course Code & Title	: EC3492 & Digital Signal Processing
Name of the Faculty member	: Mr. S.V.Ramanan
Name of the Topic:	: DSP Architecture
Name of the Innovative Practice	: Demo Based Learning
Date & Time	: 02-05-2023 & 09:00am -9.50am

#### **Description:**

Learning Outcomes: Students will be able to understand the different types of DSP Architecture.

#### **Use of appropriate method:**

#### **Justification for choosing Activity:**

Students were able to acquire more knowledge about the architectures.

#### **Effective presentation:**

The Fixed- and Floating-point architectures were discussed in classroom via board and presentation. Now by seeing the processor they can able to relate the theoretical knowledge with practical.

#### **Reflective Critique:**

By Seeing the kit Students were interested to know about the TMS Processor

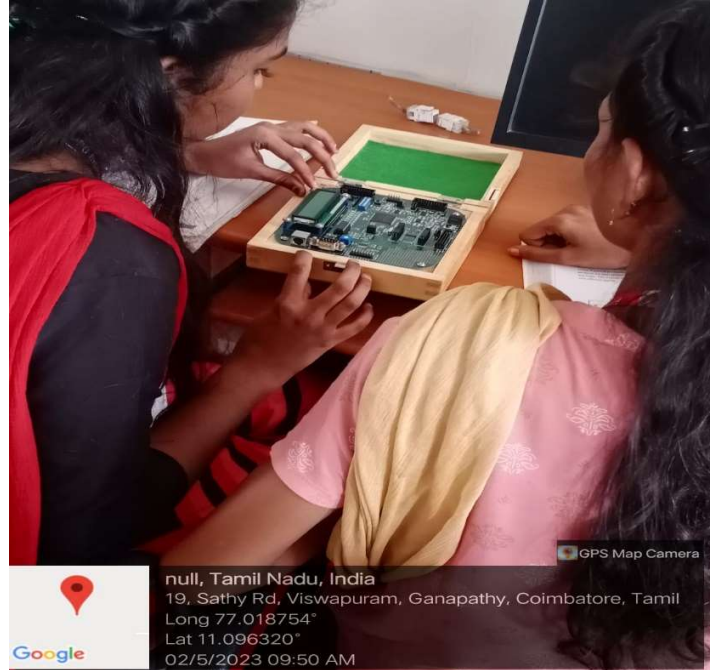
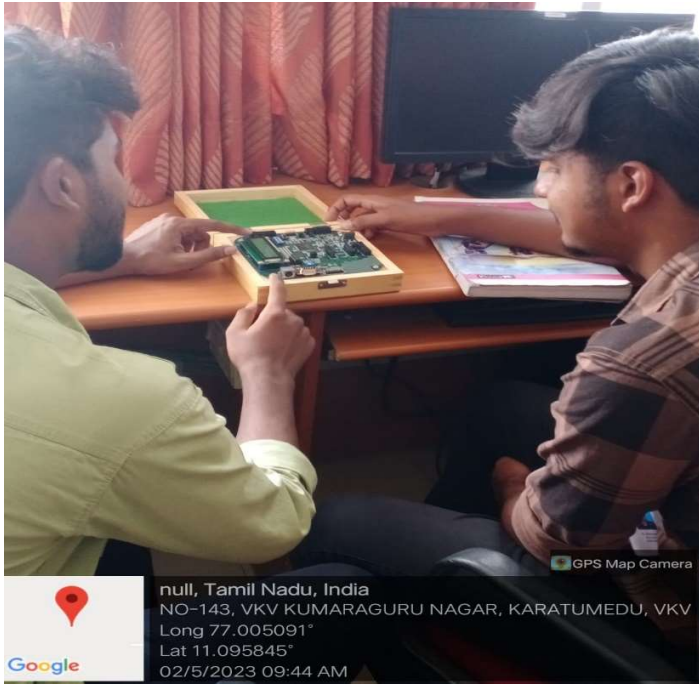
#### **Challenges:**

During theoretical class students were able to acquire minimum knowledge about the architecture.

#### **Benefits:**

Students implemented up sampler and down sampler using DSP Processor.

## Activity Photo's:



## CO & PO Mapping:

Course Outcome	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO4	3	3	2	2	3	-	1	-	1	1	1	2

## References:

1. John G. Proakis and Dimitris G. Manolakis, Digital Signal Processing – Principles, Algorithms and Applications, Fourth Edition, Pearson Education / Prentice Hall, 2007. .
2. Sanjit K. Mitra, "Digital Signal Processing – A Computer Based Approach", Tata Mc Graw Hill, 2007.
3. <https://www.ti.com/lit/an/spra396/spra396.pdf>

Signature of Faculty Member

HOD